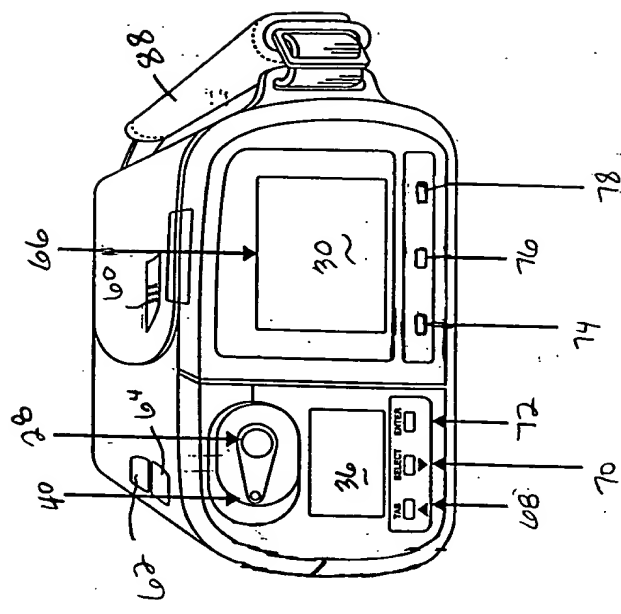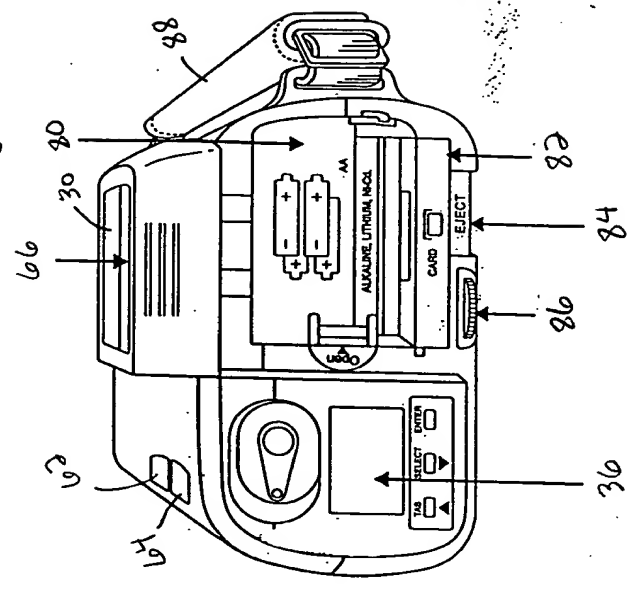# Figure 1 - Camera Block Diagram

Fig 2B

Fig 2C

Fig 2A

Figure 3 - Example Flow Diagram

User turns on camera — *100*

↓

Image LCD is off
Ready light is on — *102*

↓

User optionally turns on
quick view feature — *104*

↓

User composes image
using optical viewfinder — *106*

↓

User presses shutter
button — *108*

↓

Sensor captures image and
stores it in DRAM buffer
memory,
Ready light is turned off — *110*

↓

Quickview on? — *112* —No→

↓ Yes

Image processing block
creates low resolution LCD
size image and stores it in
buffer memory — *114*

↓

LCD is turned on,
Image is displayed
for set period of time — *116*

↓

LCD is turned off — *118*

Image processing block begins
processing high resolution image and
storaging processed image on
memory card — *120*

↓

User presses erase
button? — *122* —No→

↓ Yes

Image processing and storage of high
resolution image is halted — *124*

↓

Partially completed image file is
erased from memory card — *126*

↓

Ready light is turned on — *128*

↑

Image processing and storage of high
resolution image is completed — *130*